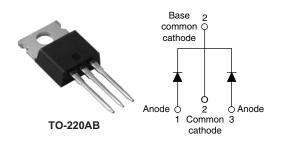
Vishay High Power Products

High Performance Schottky Generation 5.0, 2 x 15 A



2 x 15 A

45 V

0.54 A

PRODUCT SUMMARY

I_{F(AV)}

 V_R

V_F at 15 A at 125 °C

FEATURES

- 175 °C high performance Schottky diode
- Very low forward voltage drop
- Extremely low reverse leakage
- Optimized V_F vs. I_R trade off for high efficiency
- · Increased ruggedness for reverse avalanche capability
- RBSOA available
- Negligible switching losses
- Submicron trench technology
- Full lead (Pb)-free and RoHS compliant devices
- Designed and qualified for industrial level

APPLICATIONS

- High efficiency SMPS
- Automotive
- High frequency switching
- Output rectification
- Reverse battery protection
- Freewheeling
- · Dc-to-dc systems
- · Increased power density systems

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL CHARACTERISTICS VALUES UNITS						
V _{RRM}		45	N			
V _F	15 Apk, T _J = 125 °C (typical, per leg)	0.50	v			
TJ	Range	- 55 to 175	٥C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	30CTT045	UNITS	
Maximum DC reverse voltage	V _R	T _J = 25 °C	45	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum averageper legforward currentper device				15		
		I _{F(AV)}	50 % duty cycle at T_{C} = 146 °C, rectangular waveform		30	
Maximum peak one cycle non-repetitive surge current per leg		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	880	A
			10 ms sine or 6 ms rect. pulse	V_{RRM} applied	220	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 5 A, L = 4.4 mH		55	mJ
Repetitive avalanche current per leg		I _{AR}	Limited by frequency of operation and time pulse duration so that $T_J < T_J$ max. I_{AS} at T_J max. as a function of time pulse See fig. 8		I _{AS} at T _J max.	A



ROHS COMPLIANT

30CTT045

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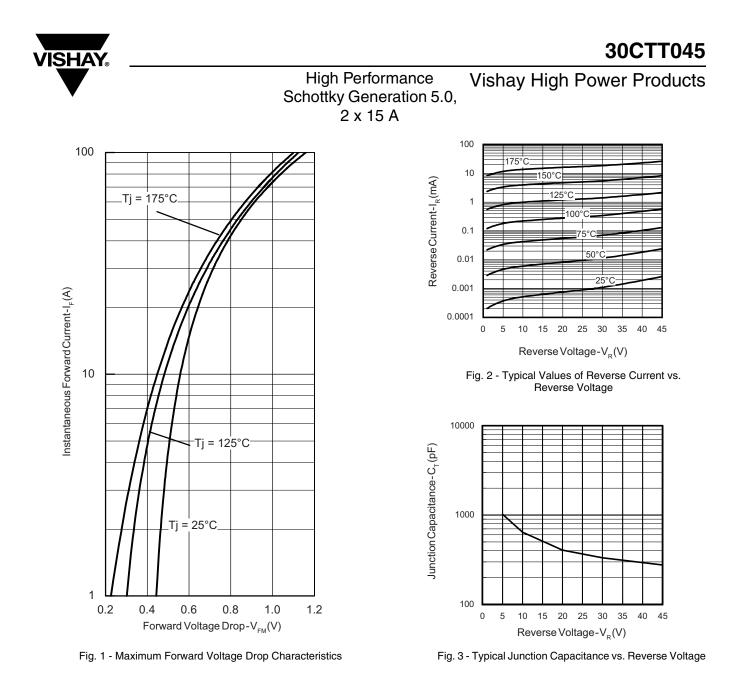


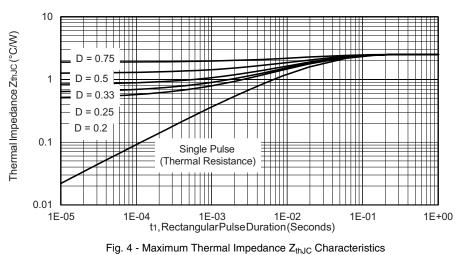
ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Forward voltage drop per leg		15 A	T.I = 25 °C	-	0.60	V
	V _{FM} ⁽¹⁾	30 A	1J=25 C	-	0.72	
	V FM \	15 A	T,I = 125 °C	-	0.54	
		30 A	1j=125 C		0.69	
Reverse leakage current per leg	I _{RM} ⁽¹⁾	$T_J = 25 \ ^{\circ}C$	$V_{\rm B}$ = Rated $V_{\rm B}$	-	100	μA
	'RM \''	$T_{\rm RM}$ $T_{\rm J} = 125 ^{\circ}{\rm C}$ $-$	-	8	mA	
Junction capacitance per leg	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 $^{\circ}\text{C}$		1020	-	pF
Series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	-	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

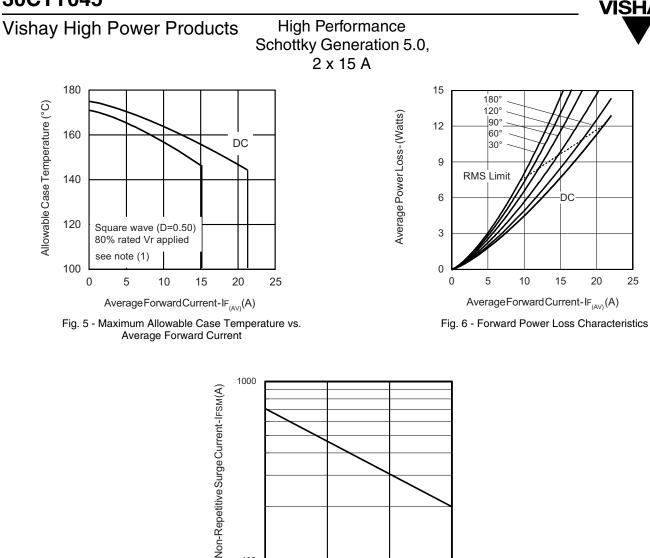
 $^{(1)}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

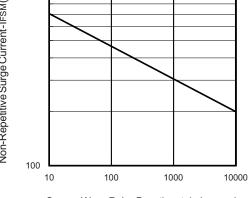
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature rang	e	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resista junction to case per leg	nce,	D		2.5		
Maximum thermal resista junction to case per devic	,	R _{thJC}	DC operation	1.25	°C/W	
Typical thermal resistance case to heatsink	9,	R _{thCS}	Mounting surface, smooth and greased	0.5		
Approvimate weight				2	g	
Approximate weight				0.07	oz.	
	minimum			6 (5)	kgf ⋅ cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device			Case style TO-220AB	30CT	T045	





30CTT045



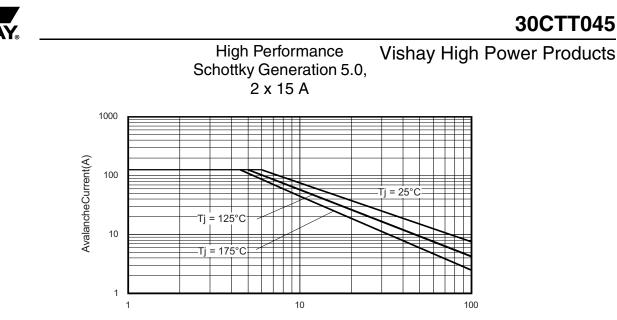


SquareWavePulseDuration-t_o(microsec)

Fig. 7 - Maximum Non-Repetitive Surge Current

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} =$ Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80$ % rated V_R



RectangularPulseDuration(µsec)

Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

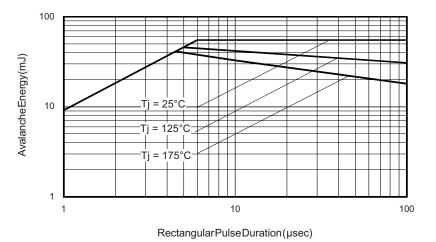


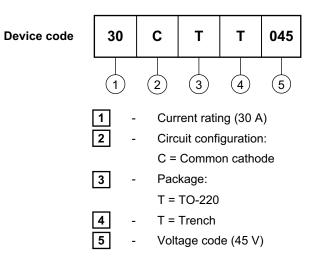
Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)

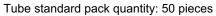
30CTT045



Vishay High Power Products High Performance Schottky Generation 5.0, 2 x 15 A

ORDERING INFORMATION TABLE





LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIMETERS		INCHES		NOTES
STWBUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



Vishay

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